### **General Description**

The MAX3224–MAX3227/MAX3244/MAX3245 are 3V-powered EIA/TIA-232 and V.28/V.24 communications interfaces with automatic shutdown/wakeup features and high data-rate capabilities.

All devices achieve a 1µA supply current using Maxim's revolutionary AutoShutdown Plus<sup>™</sup> feature. These devices automatically enter a low-power shutdown mode when the RS-232 cable is disconnected or the transmitters of the connected peripherals are inactive, and the UART driving the transmitter inputs is inactive for more than 30 seconds. They turn on again when they sense a valid transition at any transmitter or receiver input. AutoShutdown Plus saves power without changes to the existing BIOS or operating system.

The MAX3225/MAX3227/MAX3245 also feature MegaBaud<sup>™</sup> operation, guaranteeing 1Mbps for high-speed applications such as communicating with ISDN modems. The MAX3224/MAX3226/MAX3244 guarantee 250kbps operation. The transceivers have a proprietary low-dropout transmitter output stage enabling true RS-232 performance from a +3.0V to +5.5V supply with a dual charge pump. The charge pump requires only four small 0.1µF capacitors for operation from a 3.3V supply. The MAX3224–MAX3227 feature a logic-level output (READY) that asserts when the charge pump is regulating and the device is ready to begin transmitting.

All devices are available in a space-saving TQFN, TSSOP, and SSOP packages.

### Applications

Notebook, Subnotebook, and Palmtop Computers

Cellular Phones

Battery-Powered Equipment

Hand-Held Equipment

Peripherals

Printers

### \_Next Generation Device Features

- For Smaller Packaging: MAX3228/MAX3229: +2.5V to +5.5V RS-232 Transceivers in UCSP™
- For Low-Voltage or Data Cable Applications: MAX3380E/MAX3381E: +2.35V to +5.5V, 1μA, 2 Tx/2 Rx RS-232 Transceivers with ±15kV ESD-Protected I/O and Logic Pins
- ♦ For Integrated ESD Protection: MAX3222E/MAX3232E/MAX3237E/MAX3241E<sup>†</sup>/ MAX3246E: ±15kV ESD-Protected, Down to 10nA, +3.0V to +5.5V, Up to 1Mbps, True RS-232 Transceivers (MAX3246E Available in UCSP)

### \_Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	PKG CODE
MAX3224CPP	0°C to +70°C	20 Plastic DIP	_
MAX3224CAP	0°C to +70°C	20 SSOP	T4477-3
MAX3224CTP	0°C to +70°C	20 Thin QFN-EP*	T2055-5
MAX3224CUP	0°C to +70°C	20 TSSOP	_
MAX3224EPP	-40°C to +85°C	20 Plastic DIP	_
MAX3224EAP	-40°C to +85°C	20 SSOP	_
MAX3224ETP	-40°C to +85°C	20 Thin QFN-EP* 5mm x 5mm	T2055-5
MAX3224EUP	-40°C to +85°C	20 TSSOP	

Ordering Information continued at end of data sheet.

\*EP = Exposed paddle.

### **Selector Guide**

PART	NO. OF DRIVERS/ RECEIVERS	GUARANTEED DATA RATE (bps)	READY OUTPUT	AUTO- SHUTDOWN PLUS
MAX3224	2/2	250k	~	~
MAX3225	2/2	1M	~	~
MAX3226	1/1	250k	~	~
MAX3227	1/1	1M	~	~
MAX3244	3/5	250k		~
MAX3245	3/5	1M	_	~

AutoShutdownPlus, MegaBaud, and UCSP are trademarks of Maxim Integrated Products, Inc.

† Covered by U.S. Patent numbers 4,636,930; 4,679, 134; 4,777,577; 4,797,899; 4,809, 152; 4,897,774; 4,999,761; 5,649,210; and other patents pending.

### ///XI/M

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

#### **ABSOLUTE MAXIMUM RATINGS**

V <sub>CC</sub> to GND V+ to GND (Note 1)	
V- to GND (Note 1)	
V+ +  V- (Note 1)	+13V
Input Voltages	
T_IN, FORCEON, FORCEOFF to GND	0.3V to +6V
R_IN to GND	±25V
Output Voltages	
T_OUT to GND	
R_OUT, INVALID, READY to GND0.3V	to (V <sub>CC</sub> + 0.3V)
Short-Circuit Duration	
T_OUT to GND	Continuous

Note 1: V+ and V- can have maximum magnitudes of 7V, but their absolute difference cannot exceed 13V.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### **ELECTRICAL CHARACTERISTICS**

 $(V_{CC} = +3V \text{ to } +5.5V, C1-C4 = 0.1\mu\text{F}, \text{ tested at } 3.3V \pm 10\%; C_L = 0.047\mu\text{F}, C2-C4 = 0.33\mu\text{F}, \text{ tested at } 5.0V \pm 10\%; T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted}. Typical values are at T_A = +25^{\circ}\text{C}.)$ 

PARAMETER	SYMBOL	CON	MIN	TYP	MAX	UNITS	
DC CHARACTERISTICS (V <sub>CC</sub>	= 3.3V or 5.0	/, T <sub>A</sub> = +25°C)					
Supply Current, AutoShutdown Plus		FORCEON = GND, F all R_IN idle, all T_IN			1	10	μA
Supply Current, Shutdown		FORCEOFF = GND			1	10	μA
Supply Current, AutoShutdown Plus Disabled		FORCEON = FORCE	$\overrightarrow{OFF} = V_{CC}$ , no load		0.3	1	mA
LOGIC INPUTS AND RECEIVE	R OUTPUTS						
Input Logic Threshold Low		T_IN, FORCEON, FO	RCEOFF			0.8	V
Input Logic Threshold High		T_IN, FORCEON,	$V_{\rm CC} = 3.3 V$	2			V
Input Logic Threshold High		FORCEOFF	$V_{\rm CC} = 5.0 V$	2.4			1
Transmitter Input Hysteresis		·			0.5		V
Input Leakage Current		T_IN, FORCEON, FORCEOFF			±0.01	±1	μA
Output Leakage Current		R_OUT (MAX3244/M/ disabled	R_OUT (MAX3244/MAX3245), receivers disabled		±0.05	±10	μA
Output Voltage Low		$I_{OUT} = 1.6 mA$				0.4	V
Output Voltage High		$I_{OUT} = -1.0 mA$		V <sub>CC</sub> - 0.6	6 V <sub>CC</sub> - 0.1		V
RECEIVER INPUTS							
Input Voltage Range				-25		+25	V
Input Threshold Low		T <sub>A</sub> = +25°C	$V_{CC} = 3.3V$	0.6	1.2		V
input miesnoid Low		IA = +23 C	$V_{\rm CC} = 5.0 V$	0.8	1.5		
Input Threshold High		$T_A = +25^{\circ}C$ $V_{CC} = 3.3V$			1.5	2.4	V
		$V_{CC} = 5.0V$			1.8	2.4	
Input Hysteresis					0.5		V
Input Resistance		$T_A = +25^{\circ}C$		3	5	7	kΩ

### **ELECTRICAL CHARACTERISTICS (continued)**

(V<sub>CC</sub> = +3V to +5.5V, C1–C4 = 0.1 $\mu$ F, tested at 3.3V ±10%; C<sub>L</sub> = 0.047 $\mu$ F, C2–C4 = 0.33 $\mu$ F, tested at 5.0V ±10%; T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C.)

PARAMETER	SYMBOL	CONDIT	MIN	ТҮР	MAX	UNITS	
TRANSMITTER OUTPUTS							I
Output Voltage Swing		All transmitter outputs loa ground	aded with 3k $\Omega$ to	±5	±5.4		V
Output Resistance		$V_{CC} = V_{+} = V_{-} = 0$ , trans	mitter outputs = $\pm 2V$	300	10M		Ω
Output Short-Circuit Current						±60	mA
Output Leakage Current		V <sub>CC</sub> = 0 or 3V to 5.5V, V <sub>C</sub> Transmitters disabled	$DUT = \pm 12V,$			±25	μA
MOUSE DRIVEABILITY (MAX32	44/MAX324	ō)					1
Transmitter Output Voltage		T1IN = T2IN = GND, T3II T3OUT loaded with $3k\Omega$ T1OUT and T2OUT loade 2.5mA each	±5			V	
AUTOSHUTDOWN PLUS (FOR	CEON = GNI	D, $\overline{\text{FORCEOFF}} = V_{\text{CC}}$					
Receiver Input Threshold to INVALID Output High		Figure 4a Positive threshold Negative threshold		-2.7		2.7	V
Receiver Input Threshold to INVALID Output Low		Figure 4a	1	-0.3		0.3	V
INVALID, READY (MAX3224–MAX3227) Output Voltage Low		I <sub>OUT</sub> = -1.6mA				0.4	V
ĪNVALID, READY (MAX3224–MAX3227) Output Voltage High		I <sub>OUT</sub> = -1.0mA		V <sub>CC</sub> - 0.6			V
Receiver Positive or Negative Threshold to INVALID High	t <sub>INVH</sub>	V <sub>CC</sub> = 5V, Figure 4b			1		μs
Receiver Positive or Negative Threshold to INVALID Low	tinvl	V <sub>CC</sub> = 5V, Figure 4b			30		μs
Receiver or Transmitter Edge to Transmitters Enabled	tw∪	V <sub>CC</sub> = 5V, Figure 5b (Note 2)			100		μs
Receiver or Transmitter Edge to Transmitters Shutdown	t <sub>AUTOSHDN</sub>	V <sub>CC</sub> = 5V, Figure 5b (Not	te 2)	15	30	60	S

#### TIMING CHARACTERISTICS—MAX3224/MAX3226/MAX3244

 $(V_{CC} = +3V \text{ to } +5.5V, C1-C4 = 0.1\mu\text{F}, \text{ tested at } 3.3V \pm 10\%; C_L = 0.047\mu\text{F}, C2-C4 = 0.33\mu\text{F}, \text{ tested at } 5.0V \pm 10\%; T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted}$ . Typical values are at T<sub>A</sub> = +25°C.)

PARAMETER	SYMBOL	CONDITION	S	MIN	ΤΥΡ	MAX	UNITS
Maximum Data Rate		$R_L = 3k\Omega$ , $C_L = 1000pF$ , one transmitter switching	250			kbps	
Receiver Propagation Delay	t <sub>PHL</sub>	R IN to R OUT, CI = 150p	E		0.15		110
neceiver Propagation Delay	t <sub>PLH</sub>	-		0.15		μs	
Receiver Output Enable Time		Normal operation (MAX324		200		ns	
Receiver Output Disable Time		Normal operation (MAX324	4 only)		200		ns
Transmitter Skew	t <sub>PHL</sub> - t <sub>PLH</sub>	(Note 3)			100		ns
Receiver Skew	tphl - tplh				50		ns
Transition-Region Slew Rate		$V_{CC} = 3.3V$ , $T_A = +25^{\circ}C$ , $R_L = 3k\Omega$ to $7k\Omega$ ,	C <sub>L</sub> = 150pF to 1000pF	6		30	V/µs
Tansilon-negion Siew Hale		measured from +3V to -3V or -3V to +3V	C <sub>L</sub> = 150pF to 2500pF	4		30	v/µS

### TIMING CHARACTERISTICS—MAX3225/MAX3227/MAX3245

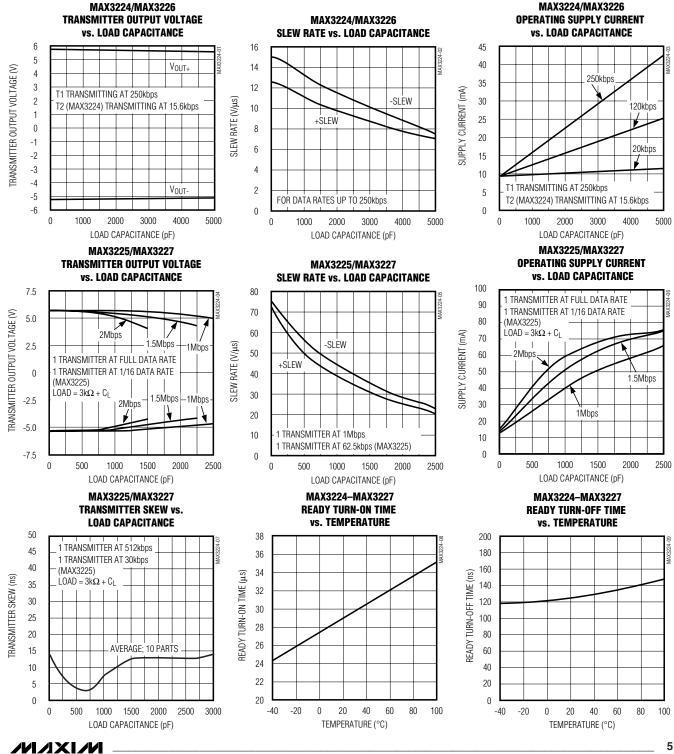
(V<sub>CC</sub> = +3V to +5.5V, C1–C4 = 0.1 $\mu$ F, tested at 3.3V ±10%; C<sub>L</sub> = 0.047 $\mu$ F, C2–C4 = 0.33 $\mu$ F, tested at 5.0V ±10%; T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
		$R_L = 3k\Omega$ , $C_L = 1000pF$ , one transmitter switching	250			
Maximum Data Rate		$V_{CC} = 3.0V$ to 4.5V, $R_L = 3k\Omega$ , $C_L = 250pF$ , one transmitter switching	1000			kbps
		$V_{CC}$ = 4.5V to 5.5V, $R_L$ = 3k $\Omega$ , $C_L$ = 1000pF, one transmitter switching	1000			
Receiver Propagation Delay	tphl	R IN to R OUT, $C_1 = 150 \text{pF}$		0.15		110
Necelver Fropagation Delay	tplh		0.15		- µs	
Receiver Output Enable Time		Normal operation (MAX3245 only)		200		ns
Receiver Output Disable Time		Normal operation (MAX3245 only)		200		ns
Transmitter Skew	tphl - tplh	(Note 3)		25		ns
Receiver Skew	tphl - tplh			50		ns
Transition-Region Slew Rate		$ \begin{array}{l} V_{CC}=3.3 \text{V}, \ T_{A}=+25^{\circ}\text{C}, \\ \text{R}_{L}=3 \text{k}\Omega \ \text{to} \ 7 \text{k}\Omega, \ \text{C}_{L}=150 \text{pF} \ \text{to} \ 1000 \text{pF}, \\ \text{measured from +3V to -3V or -3V to +3V} \end{array} $	24		150	V/µs

**Note 2:** A transmitter/receiver edge is defined as a transition through the transmitter/receiver input logic thresholds. **Note 3:** Transmitter skew is measured at the transmitter zero cross points.

#### **Typical Operating Characteristics**

 $(V_{CC} = +3.3V, 250kbps data rate, C1-C4 = 0.1\mu F, all transmitters loaded with 3k\Omega and CL, T_A = +25^{\circ}C, unless otherwise noted.)$ 

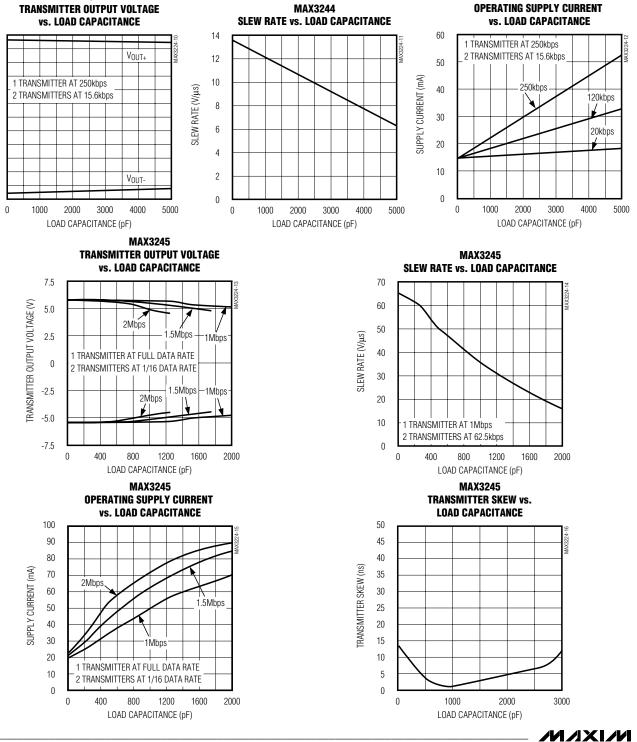




MAX3244

 $(V_{CC} = +3.3V, 250kbps data rate, C1-C4 = 0.1\mu F, all transmitters loaded with 3k\Omega and CL, T_A = +25^{\circ}C, unless otherwise noted.)$ 

MAX3224-MAX3227/MAX3244/MAX3245 MAX3244 6 5 TRANSMITTER OUTPUT VOLTAGE (V) 4 3 2 1 0 -1 -2 -3 -4 -5 -6 0 1000 2000 7.5 TRANSMITTER OUTPUT VOLTAGE (V) 5.0 2.5 0 -2.5 -5.0 -7.5 0



6

### **Pin Description**

		PIN			
MAX3 MAX3		MAX3226	MAX3244	NAME	FUNCTION
DIP/ SSIP/ TSSOP	TQFN	MAX3220 MAX3227	MAX3244 MAX3245		
1	19	1	_	READY	Ready to Transmit Output, Active High. READY is enabled high when V- goes below -4V and the device is ready to transmit.
2	1	2	28	C1+	Positive Terminal of Voltage-Doubler Charge-Pump Capacitor
3	20	3	27	V+	+5.5V Generated By the Charge Pump
4	2	4	24	C1-	Negative Terminal of Voltage-Doubler Charge-Pump Capacitor
5	3	5	1	C2+	Positive Terminal of Inverting Charge-Pump Capacitor
6	4	6	2	C2-	Negative Terminal of Inverting Charge-Pump Capacitor
7	5	7	3	V-	-5.5V Generated By the Charge Pump
8, 17	6, 15	13	9, 10, 11	T_OUT	RS-232 Transmitter Outputs
9, 16	7, 14	8	4–8	R_IN	RS-232 Receiver Inputs
10, 15	8, 13	9	15–19	R_OUT	TTL/CMOS Transmitter Outputs
11	9	10	21	INVALID	Valid Signal Detector Output, Active Low. A logic-high indicates that a valid RS-232 level is present on a receiver input.
12, 13	10, 11	11	12, 13, 14	T_IN	TTL/CMOS Transmitter Inputs
14	12	12	23	FORCEON	Force-On Input, Active High. Drive high to override AutoShutdown Plus, keeping transmitters and receivers on (FORCEOFF must be high) (Table 1).
18	16	14	25	GND	Ground
19	17	15	26	V <sub>CC</sub>	+3.0V to +5.5V Single Supply Voltage
20	18	16	22	FORCEOFF	Force-Off Input, Active Low. Drive low to shut down transmitters, receivers (except R2OUTB), and charge pump. This overrides AutoShutdown Plus and FORCEON (Table 1).
	_	_	20	R2OUTB	TTL/CMOS Noninverting Complementary Receiver Outputs. Always active.
	EP	_		EP	Exposed Paddle. Solder the exposed paddle to the ground, or leave unconnected.

#### \_Detailed Description

#### **Dual Charge-Pump Voltage Converter**

The MAX3224–MAX3227/MAX3244/MAX3245's internal power supply consists of a regulated dual charge pump that provides output voltages of +5.5V (doubling charge pump) and -5.5V (inverting charge pump), over the +3.0V to +5.5V range. The charge pump operates in discontinuous mode: if the output voltages are less than 5.5V, the charge pump is enabled; if the output

voltages exceed 5.5V, the charge-pump is disabled. Each charge pump requires a flying capacitor (C1, C2) and a reservoir capacitor (C3, C4) to generate the V+ and V- supplies.

The READY output (MAX3224–MAX3227) is low when the charge pumps are disabled in shutdown mode. The READY signal asserts high when V- goes below -4V.

LapLink is a trademark of Traveling Software.



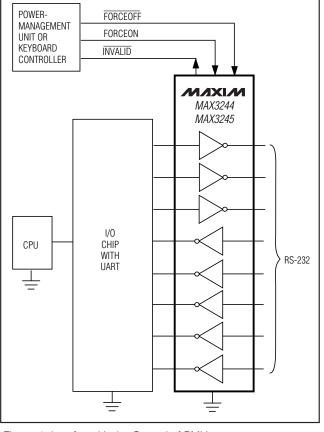


Figure 1. Interface Under Control of PMU

#### **RS-232 Transmitters**

The transmitters are inverting level translators that convert CMOS-logic levels to 5.0V EIA/TIA-232 levels. The MAX3224/MAX3226/MAX3244 guarantee a 250kbps data rate (1Mbps for the MAX3225/MAX3227/ MAX3245) with worst-case loads of  $3k\Omega$  in parallel with 1000pF, providing compatibility with PC-to-PC communication software (such as LapLink<sup>TM</sup>). Transmitters can be paralleled to drive multiple receivers. Figure 1 shows a complete system connection.

When FORCEOFF is driven to ground or when the Auto-Shutdown Plus circuitry senses that all receiver and transmitter inputs are inactive for more than 30sec, the transmitters are disabled and the outputs go into a high-impedance state. When powered off or shut down, the outputs can be driven to  $\pm 12V$ . The transmitter inputs do not have pull-up resistors. Connect unused inputs to GND or V<sub>CC</sub>.

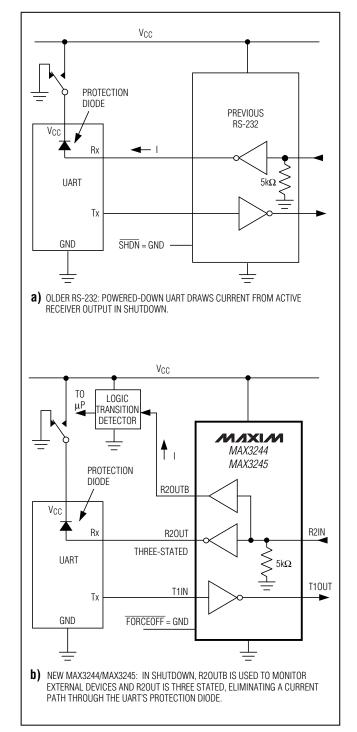


Figure 2. The MAX3244/MAX3245 detect RS-232 activity when the UART and interface are shut down.



Table 1.	Output	Control	Truth	Table
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OPERATION STATUS	FORCEON	FORCEOFF	VALID RECEIVER LEVEL	RECEIVER OR TRANSMITTER EDGE WITHIN 30sec	T_OUT	R_OUT (MAX3224/ MAX3225/ MAX3226/ MAX3227)	R_OUT (MAX3244/ MAX3245)	R2OUTB (MAX3244/ MAX3245)
Shutdown (Forced Off)	Х	0	Х	Х	High-Z	Active	High-Z	Active
Normal Operation (Forced On)	1	1	Х	Х	Active	Active	Active	Active
Normal Operation (AutoShutdown Plus)	0	1	Х	Yes	Active	Active	Active	Active
Shutdown (Auto- Shutdown Plus)	0	1	Х	No	High-Z	Active	Active	Active
Normal Operation	INVALID*	1	Yes	Х	Active	Active	Active	Active
Normal Operation	INVALID*	1	Х	Yes	Active	Active	Active	Active
Shutdown	INVALID*	1	No	No	High-Z	Active	Active	Active
Normal Operation (AutoShutdown)	INVALID*	INVALID**	Yes	Х	Active	Active	Active	Active
Shutdown (AutoShutdown)	INVALID*	INVALID**	No	Х	High-Z	Active	High-Z	Active

X = Don't care

\* INVALID connected to FORCEON

\*\* INVALID connected to FORCEON and FORCEOFF

#### **RS-232 Receivers**

The receivers convert RS-232 signals to CMOS-logic output levels. The MAX3224–MAX3227 feature inverting outputs that always remain active (Table 1). The MAX3244/MAX3245 have inverting three-state outputs that are high impedance when shut down (FORCEOFF = GND) (Table 1).

The MAX3244/MAX3245 feature an extra, always active, noninverting output, R2OUTB. R2OUTB output monitors receiver activity while the other receivers are high impedance, allowing Ring Indicator applications to be monitored without forward biasing other devices connected to the receiver outputs. This is ideal for systems where V<sub>CC</sub> is set to ground in shutdown to accommodate peripherals such as UARTs (Figure 2).

The MAX3224–MAX3227/MAX3244/MAX3245 feature an INVALID output that is enabled low when no valid RS-232 voltage levels <u>have</u> been detected on all receiver inputs. Because INVALID indicates the receiver input's condition, it is independent of FORCEON and FORCEOFF states (Figures 3 and 4).

#### **AutoShutdown Plus Mode**

The MAX3224–MAX3227/MAX3244/MAX3245 achieve a 1 $\mu$ A supply current with Maxim's AutoShutdown Plusfeature, which operates when FORCEOFF is high and a FORCEON is low. When these devices do not sense a valid signal transition on any receiver and transmitter input for 30sec, the on-board charge pumps are shut down, reducing supply current to 1 $\mu$ A. This occurs if the RS-232 cable is disconnected or if the connected



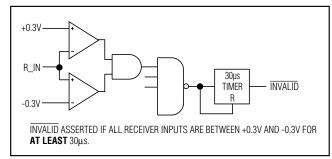


Figure 3a. INVALID Functional Diagram, INVALID Low

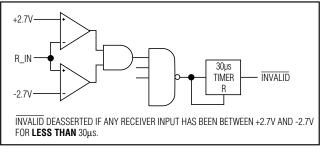


Figure 3b. INVALID Functional Diagram, INVALID High

#### Table 2. INVALID Truth Table

RS-232 SIGNAL PRESENT AT ANY RECEIVER INPUT	INVALID OUTPUT
Yes	High
No	Low

peripheral transmitters are turned off, and the UART driving the transmitter inputs is inactive. The system turns on again when a valid transition is applied to any RS-232 receiver or transmitter input. As a result, the system saves power without changes to the existing BIOS or operating system.

Figures 3a and 3b depict valid and invalid RS-232 receiver voltage levels. INVALID indicates the receiver input's condition, and is independent of FORCEON and FORCEOFF states. Figure 3 and Tables 1 and 2 summarize the operating modes of the MAX3224–MAX3227/MAX3244/MAX3245 devices. FORCEON and FORCEOFF override AutoShutdown Plus circuitry. When neither control is asserted, the IC selects

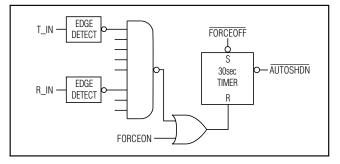
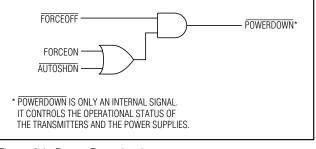


Figure 3c. AutoShutdown Plus Logic





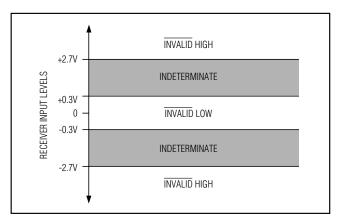


Figure 4a. Receiver Positive/Negative Thresholds for INVALID

between these states automatically based on the last receiver or transmitter input edge received.

When shut down, the device's charge pumps turn off, V+ is pulled to V<sub>CC</sub>, V- is pulled to ground, the transmitter outputs are high impedance, and READY (MAX3224–MAX3227) is driven low. The time required to exit shutdown is typically 100 $\mu$ s (Figure 8).



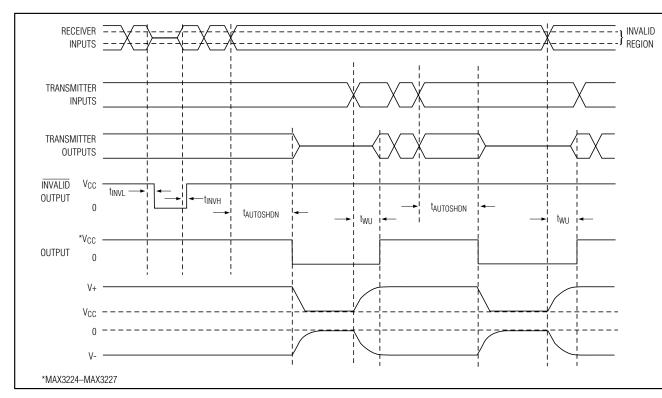


Figure 4b. AutoShutdown Plus, INVALID, and READY Timing Diagram

By connecting FORCEON to INVALID, the MAX3224– MAX3227/MAX3244/MAX3245 shut down when no valid receiver level and no receiver or transmitter edge is detected for 30sec, and wake up when a valid receiver level or receiver or transmitter edge is detected.

By connecting FORCEON and FORCEOFF to INVALID, the MAX3224–MAX3227/MAX3244/MAX3245 shutdown when no valid receiver level is detected and wake up when a valid receiver level is detected (same functionality as AutoShutdown feature on MAX3221/MAX3223/ MAX3243).

A mouse or other system with AutoShutdown Plus may need time to wake up. Figure 5 shows a circuit that forces the transmitters on for 100ms, allowing enough time for the other system to realize that the MAX3244/ MAX3245 is awake. If the other system outputs valid RS-232 signal transitions within that time, the RS-232 ports on both systems remain enabled.

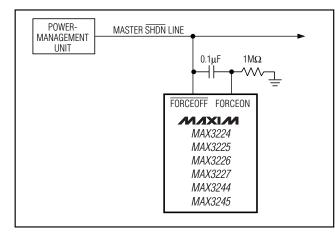


Figure 5. AutoShutdown Plus Initial Turn-On to Wake Up a Mouse or Another System

#### **Software-Controlled Shutdown**

If direct software control is desired, use INVALID to indicate DTR or Ring Indicator signal. Tie FORCEOFF and FORCEON together to bypass the AutoShutdown Plus so the line acts like a SHDN input.

### **Applications Information**

#### **Capacitor Selection**

The capacitor type used for C1–C4 is not critical for proper operation; polarized or nonpolarized capacitors can be used. The charge pump requires  $0.1\mu$ F capacitors for 3.3V operation. For other supply voltages, see Table 3 for required capacitor values. Do not use values smaller than those listed in Table 3. Increasing the capacitor values (e.g., by a factor of 2) reduces ripple on the transmitter outputs and slightly reduces power consumption. C2, C3, and C4 can be increased without changing C1's value. However, do not increase C1 without also increasing the values of C2, C3, C4, and CBYPASS, to maintain the proper ratios (C1 to the other capacitors).

When using the minimum required capacitor values, make sure the capacitor value does not degrade excessively with temperature. If in doubt, use capacitors with a larger nominal value. The capacitor's equivalent series resistance (ESR), which usually rises at low temperatures, influences the amount of ripple on V+ and V-.

#### **Power-Supply Decoupling**

In most circumstances, a  $0.1\mu F$  V<sub>CC</sub> bypass capacitor is adequate. In applications that are sensitive to power-supply noise, use a capacitor of the same value as charge-pump capacitor C1. Connect bypass capacitors as close to the IC as possible.

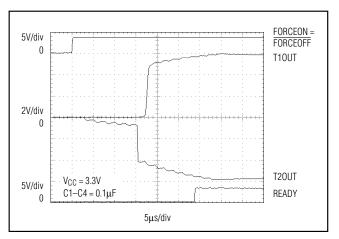
# Transmitter Outputs when Exiting Shutdown

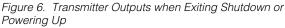
Figure 6 shows two transmitter outputs when exiting shutdown mode. As they become active, the two transmitter outputs are shown going to opposite RS-232 levels (one transmitter input is high; the other is low). Each

Table 3. Required Minimum CapacitanceValues

V <sub>CC</sub> (V)	C1, CBYPASS (µF)	C2, C3, C4 (μF)
3.0 to 3.6	0.1	0.1
4.5 to 5.5	0.047	0.33
3.0 to 5.5	0.22	1

transmitter is loaded with  $3k\Omega$  in parallel with 1000pF. The transmitter outputs display no ringing or undesirable transients as they come out of shutdown. Note that the transmitters are enabled only when the magnitude of V- exceeds approximately -3V.





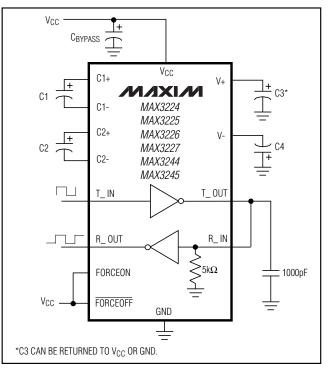


Figure 7. Loopback Test Circuit



#### **High Data Rates**

The MAX3224/MAX3226/MAX3244 maintain the RS-232 ±5.0V minimum transmitter output voltage even at high data rates. Figure 7 shows a transmitter loopback test circuit. Figure 8 shows a loopback test result at 120kbps, and Figure 9 shows the same test at 250kbps. For Figure 8, all transmitters were driven simultaneously at 120kbps into RS-232 loads in parallel with 1000pF. For Figure 9, a single transmitter was driven at 250kbps, and all transmitters were loaded with an RS-232 receiver in parallel with 250pF.

The MAX3225/MAX3227/MAX3245 maintain the RS-232  $\pm$ 5.0V minimum transmitter output voltage at data rates up to 1Mbps (MegaBaud). Figure 10 shows a loopback test result with a single transmitter driven at 1Mbps and all transmitters loaded with an RS-232 receiver in parallel with 250pF.

#### Mouse Driveability

The MAX3244/MAX3245 are specifically designed to power serial mice while operating from low-voltage power supplies. They have been tested with leading mouse brands from manufacturers such as Microsoft and Logitech. The MAX3244/MAX3245 successfully

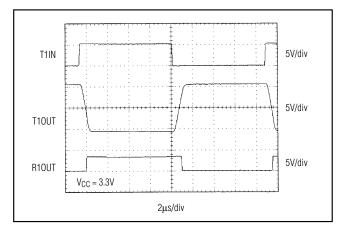


Figure 8. MAX3224/MAX3226/MAX3244 Loopback Test Result at 120kbps

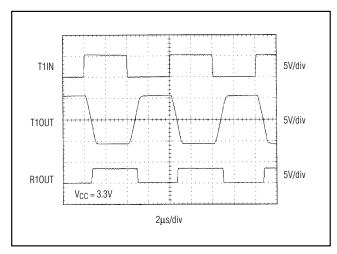


Figure 9. MAX3224/MAX3226/MAX3244 Loopback Test Result at 250kbps

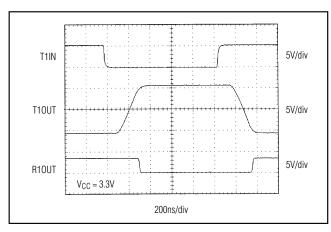


Figure 10. MAX3225/MAX3227/MAX3245 Loopback Test Result at 1Mbps

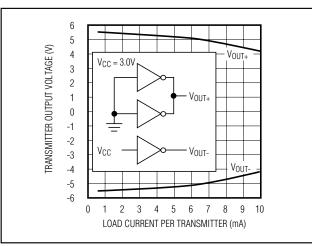


Figure 11a. MAX324\_Transmitter Output Voltage vs. Load Current per Transmitter

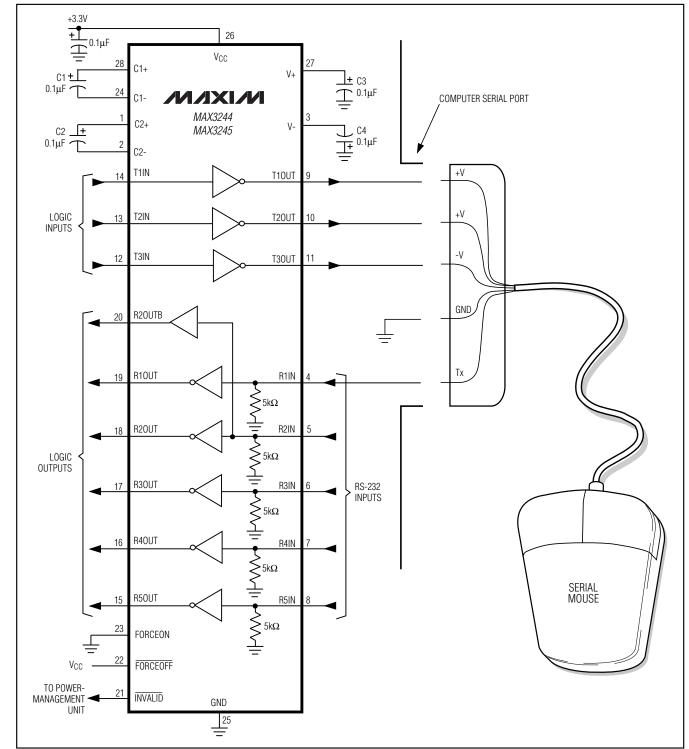


Figure 11b. Mouse Driver Test Circuit

MAX3224-MAX3227/MAX3244/MAX3245\*

drove all serial mice tested and met their respective current and voltage requirements. The MAX3244/ MAX3245 dual charge pump ensures the transmitters will supply at least ±5V during worst-case conditions. Figure 11a shows the transmitter output voltages under increasing load current. Figure 11b shows a typical mouse connection.

#### Interconnection with 3V and 5V Logic

The MAX3224–MAX3227/MAX3244/MAX3245 can directly interface with various 5V logic families, including ACT and HCT CMOS. See Table 4 for more information on possible combinations of interconnections.

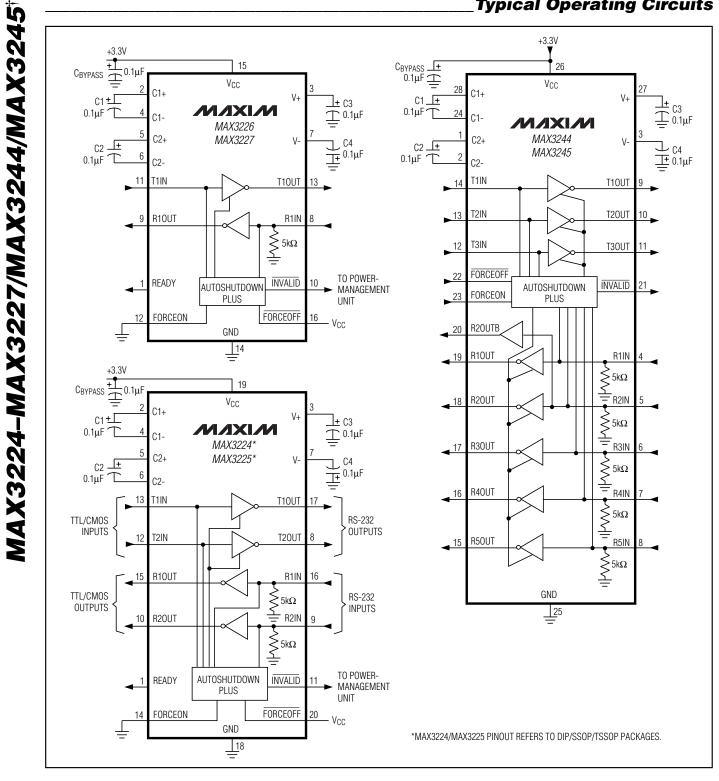
Table 5 lists other Maxim 3.0V to 5.5V powered transceivers.

#### Table 4. Logic Family Compatibility with Various Supply Voltages

SYSTEM POWER-SUPPLY VOLTAGE (V)	V <sub>CC</sub> SUPPLY VOLTAGE (V)	COMPATIBILITY
3.3	3.3	Compatible with all CMOS families
5	5	Compatible with all TTL and CMOS families
5	3.3	Compatible with ACT and HCT CMOS, and with AC, HC, or CD4000 CMOS

#### Table 5. 3.0V to 5.5V Powered RS-232 Transceivers from Maxim

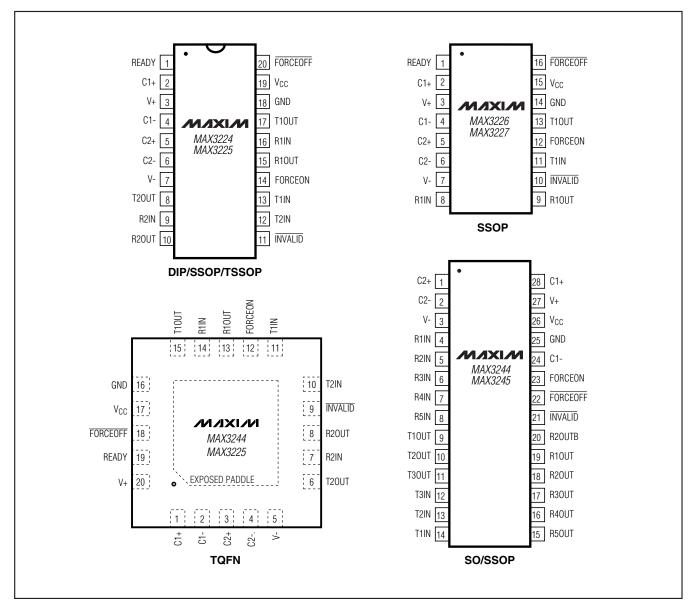
PART	SUPPLY VOLTAGE RANGE (V)	NO. OF Tx/Rx	SUPPLY CURRENT (µA)	AUTO- SHUTDOWN PLUS	AUTO- SHUTDOWN	GUARANTEED DATA RATE (bps)
MAX3241	+3.0 to +5.5	3/5	300	—		120k
MAX3243	+3.0 to +5.5	3/5	1	—	Yes	120k
MAX3244	+3.0 to +5.5	3/5	1	Yes		250k
MAX3245	+3.0 to +5.5	3/5	1	Yes		1M
MAX3232	+3.0 to +5.5	2/2	300	_		120k
MAX3222	+3.0 to +5.5	2/2	300	—		120k
MAX3223	+3.0 to +5.5	2/2	1	—	Yes	120k
MAX3224	+3.0 to +5.5	2/2	1	Yes		250k
MAX3225	+3.0 to +5.5	2/2	1	Yes		1M
MAX3221	+3.0 to +5.5	1/1	1	—	Yes	120k
MAX3226	+3.0 to +5.5	1/1	1	Yes		250k
MAX3227	+3.0 to +5.5	1/1	1	Yes		1M



#### **Typical Operating Circuits**

M/IXI/M

#### **Pin Configurations**



PART	TEMP RANGE	PIN-PACKAGE	PKG CODE
MAX3225CPP	0°C to +70°C	20 Plastic DIP	_
MAX3225CAP	0°C to +70°C	20 SSOP	_
MAX3225CTP	0°C to +70°C	20 Thin QFN-EP*	T2055-5
MAX3225CUP	0°C to +70°C	20 TSSOP	_
MAX3225EPP	-40°C to +85°C	20 Plastic DIP	_
MAX3225EAP	-40°C to +85°C	20 SSOP	_
MAX3225ETP	-40°C to +85°C	20 Thin QFN-EP*	T2055-5
MAX3225EUP	-40°C to +85°C	20 TSSOP	_
MAX3226CAE	0°C to +70°C	16 SSOP	_
MAX3226EAE	-40°C to +85°C	16 SSOP	_
MAX3227CAE	0°C to +70°C	16 SSOP	_
MAX3227EAE	-40°C to +85°C	16 SSOP	_
MAX3244CWI	0°C to +70°C	28 Wide SO	_
MAX3244CAI	0°C to +70°C	28 SSOP	_
MAX3244EWI	-40°C to +85°C	28 Wide SO	_
MAX3244EAI	-40°C to +85°C	28 SSOP	_
MAX3245CWI	0°C to +70°C	28 Wide SO	_
MAX3245CAI	0°C to +70°C	28 SSOP	_
MAX3245EWI	-40°C to +85°C	28 Wide SO	_
MAX3245EAI	-40°C to +85°C	28 SSOP	_
* CD Cypanad			

#### **Ordering Information(continued)**

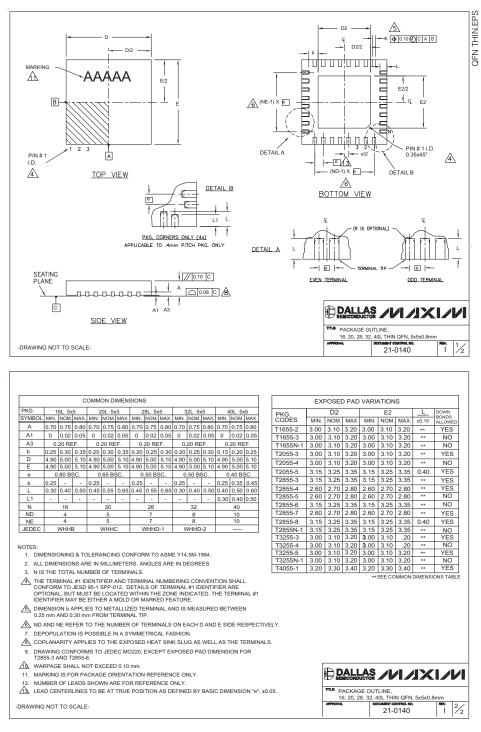
### Chip Information

MAX3224 TRANSISTOR COUNT: 1129 MAX3225 TRANSISTOR COUNT: 1129 MAX3226 TRANSISTOR COUNT: 1129 MAX3227 TRANSISTOR COUNT: 1129 MAX3244/MAX3245 TRANSISTOR COUNT: 1335 PROCESS: BICMOS

\*EP = Exposed paddle.

#### Package Information

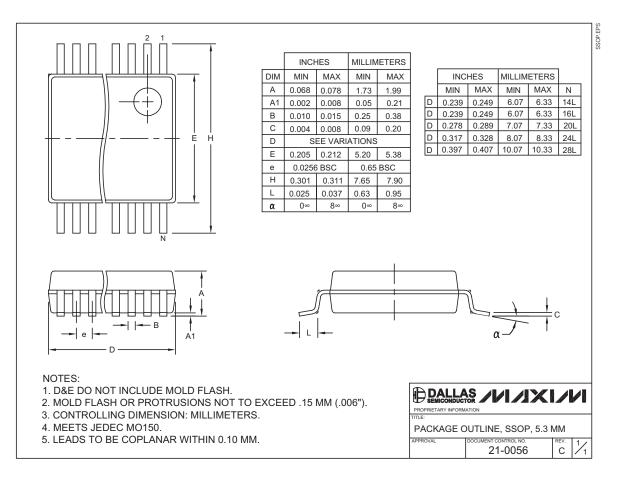
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to <u>www.maxim-ic.com/packages</u>.)





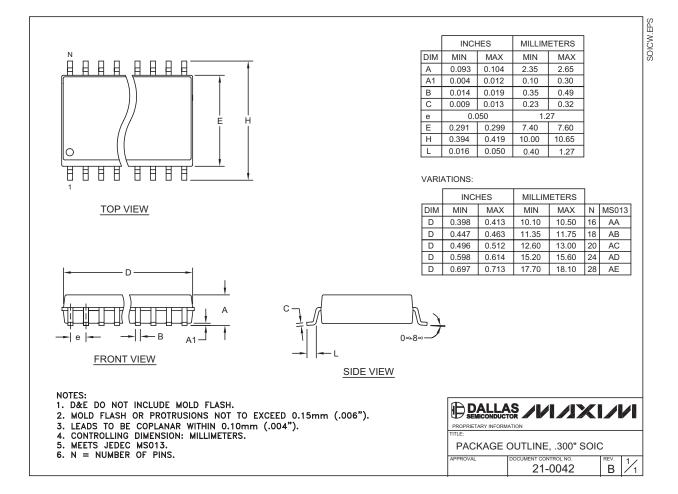
#### **Package Information**

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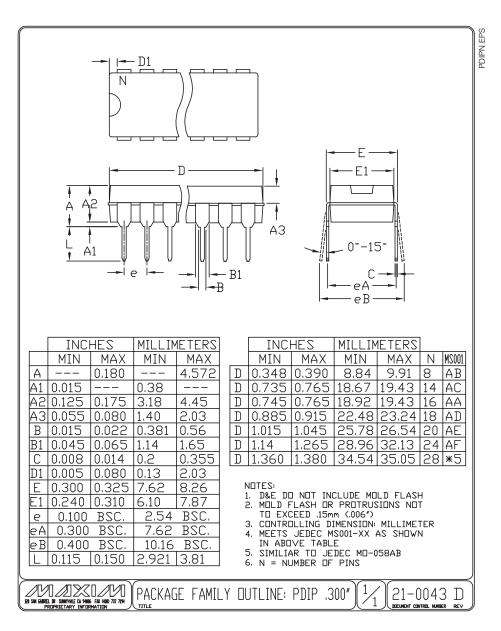
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